

CURRICULUM VITÆ

CONTACT DETAILS

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PERSONAL DETAILS

Date of birth: 4 Nov 1977

Place of birth: Lodz, Poland

Citizenship: Polish

Marital status: Single

Military service: Regulated

EDUCATION

- 28/09/2010 *Doctor of Philosophy (Technical Science) degree in Electronics*
Thesis: "Modelling of high power semiconductor devices with special consideration of structure spatial parameters" – Lodz University of Technology, Faculty of Electrical, Electronic, Computer and Control Engineering
- 2001–2006 Doctoral study – Lodz University of Technology
- 25/09/2001 *Master of Science (Engineering) degree in Electronic appliances (with honours)*
Thesis: "Problems of PIN diode modelling in CAD software" – Lodz University of Technology, Faculty of Electrical and Electronic Engineering
- 1996–2001 Full-time master study in Electronics and Telecommunications – Lodz University of Technology
- 1991–1996 N. Copernicus Secondary School, Lodz, Poland
Mathematics and physics profile

PROFESSIONAL EXPERIENCE

- since 10/2002 Lodz University of Technology, Department of Microelectronics and Computer Science – Electronic Technician, Senior Technical Assistant, Assistant Lecturer, Lecturer (since 11/2010)
- Functions: Supervisor, Power Electronics Laboratory; Coordinator, cooperation with Philips Lighting Poland; Professional training supervisor; Member, Faculty Council (2004–2005); Member, Tender Board

TRAINING

- 2012 *Most frequent mistakes in public procurement within EU-funded projects. Advanced training* – ApexNet, Lodz, Poland
- 2011 *Electromagnetic Compatibility of Integrated Circuits* – Fondation Suisse pour

la Recherche en Microtechnique, Neuchâtel, Switzerland

2002–2003 *Teaching Excellence Study* – Technical University of Lodz; official Polish teaching qualifications

RESEARCH FELLOWSHIPS

2012–2013 Kafedra System Avtomatyzovanoho Proektuvannia, Natsional'nyi Universytet L'vivs'ka Politekhnikha, L'viv, Ukraine (3 months)

2012 Vakgroep voor Elektronica en Informatiesystemen, Universiteit Gent, Gent, Belgium (2 weeks)

2000–2001 Laboratoire d'Analyse et d'Architecture des Systèmes, Toulouse, France (6 months)

SKILLS

- Operation systems (user level): Microsoft Windows, Linux, Unix
- Programming: C, C++, Pascal, assemblers, Tcl/Tk; HTML, JavaScript, CSS, PHP, SQL; basic knowledge of: Java, Verilog, VHDL/VHDL-AMS; microcontrollers: Atmel AVR, Texas Instruments MSP, Microchip PIC/dsPIC; PLC industrial controllers
- Software: Cadence/PSpice, Altium Designer, Matlab, Scilab, AutoCAD
- Office suites: Microsoft Office & Visual Basic, LibreOffice & Star Basic
- Languages: Polish – native; English, Ukrainian, French – very good; Russian – good; German, Catalan – basic communication
- Analytical thinking and organisational abilities
- Flexibility, diligence, sense of duty

MEMBERSHIP

Institute of Electrical and Electronic Engineers (IEEE), Member (since 2005)

PUBLIC ACTIVITY

2003–2006 Doctoral Self-Government, Lodz University of Technology – Vice-President, Doctoral Council; Deputy, National Doctoral Representation

HOBBIES

Active tourism, history and culture of Central-Eastern Europe, history of Lodz, 19th/20th century painting and architecture, public transport