

PUBLICATIONS

Journal Papers

- [1] Starzak Ł., Bek S., Olszewski A.: Poprawa parametrów energetycznych układów zasilania lamp fluorescencyjnych. *Zeszyty Naukowe Politechniki Łódzkiej. Nr 962*. Łódź: Wydawnictwo Politechniki Łódzkiej, 2005, pp. 129–138. (IV Krajowe Sympozjum Kompatybilność Elektromagnetyczna w Elektrotechnice i Elektronice EMC'05.) ISSN 0374-4817.
- [2] Bek S., Poźniak T., Jabłoński G., Starzak Ł.: Zagadnienia filtracji harmonicznych w układach aktywnej korekcji współczynnika mocy. *Zeszyty Naukowe Politechniki Łódzkiej. Nr 962*. Łódź: Wydawnictwo Politechniki Łódzkiej, 2005, pp. 155–164. (IV Krajowe Sympozjum Kompatybilność Elektromagnetyczna w Elektrotechnice i Elektronice EMC'05.) ISSN 0374-4817.
- [3] Grecki M., Jankowski M., Makowski D., Napieralska M., Olszewski A., Owczarek M., Pietrzak P., Sankowski W., Sękalski P., Starzak Ł., Zubert M., Napieralski A.: Katedra Mikroelektroniki i Technik Informatycznych PŁ – dla gospodarki. *Biuletyn Techniczno-Informacyjny Zarządu Oddziału Łódzkiego Stowarzyszenia Elektryków Polskich*. [Łódź]: Vol. 32, No. 3, 2006, pp. 32-36. ISSN 1428-8966.
- [4] Starzak Ł., Bek S., Modelowanie kompaktowych lamp fluorescencyjnych do badań ich oddziaływania na sieć zasilającą. *Przegląd Elektrotechniczny*. Vol. 83, No. 9, 2007, pp. 106–107. (V Krajowe Sympozjum „Kompatybilność elektromagnetyczna w elektrotechnice i elektronice” EMC '07, 4–5 października 2007.) ISSN 0033-2097.
- [5] Starzak Ł., Napieralski A.: IGBT static model based on diffusion equation implemented in SPICE source code. *Elektronika*. Vol. 52, No. 3, 2011, pp. 41–43. ISSN 0033-2089.
- [6] Starzak Ł., Grabowski K., Poźniak T.: Zaburzenia generowane w przetwornicy impulsowej o wysokiej częstotliwości przełączania zawierającej diodę z węgla krzemu. *Przegląd Elektrotechniczny*. Vol. 88, No. 2, 2012, pp. 48–50. ISSN 0033-2097.
- [7] Starzak Ł.: Praca ze zmienną częstotliwością przełączania jako metoda redukcji amplitud zaburzeń w przekształtnikach impulsowych. *Przegląd Elektrotechniczny*. Vol. 88, No. 2, 2012, pp. 51–53. ISSN 0033-2097.
- [8] Zubert M., Starzak Ł., Jablonski G., Napieralska M., Janicki M., Pozniak T., Napieralski A.: An accurate electro-thermal model for merged SiC PiN Schottky diodes. *Microelectronics Journal [IF 0,919]*. Vol. 43, Issue 5, 2012, pp. 312–320. ISSN 0026-2692.
- [9] Starzak Ł., Zubert M., Janicki M., Torzewicz T., Napieralska M., Jabłoński G., Napieralski A.: Behavioral approach to SiC MPS diode electrothermal model generation. *IEEE Transactions on Electron Devices [IF 2,318]*. Vol. 60, No. 2, February 2013, pp. 630–638. ISSN 0018-9383.
- [10] Napieralski A., Napieralska M., Starzak Ł., Zubert M.: Modern high power semiconductor devices. *Proceedings of SPIE*. Vol. 8902, 2013, pp. 890203-1–890203-17. (Electron Technology Conference 2013, Ryn, Poland, 16–20 April 2013.) ISSN 0277-786X.
- [11] Zubert M., Napieralski A., Napieralska M., Jabłoński G., Starzak Ł., Janicki M.: The behavioral approach to silicon carbide power components modeling. *Proceedings of SPIE*. Vol. 8902, 2013, pp. 890206-1–890206-8. (Electron Technology Conference 2013, Ryn, Poland, 16–20 April 2013.) ISSN 0277-786X.

Conference Papers

- [12] Starzak Ł., Zubert M., Napieralski A., Austin P., Bonnet G., Bordignon Th., Marmouget M., Sanchez J.-L.: Physical power diode model and its implementation in Saber environment. In: *Proceedings of the 8th International Conference “Mixed Design of Integrated Circuits and Systems” (MIXDES)*. Zakopane, Poland, 21–23 June 2001. Łódź: Lodart, 2001, pp. 213–220. ISBN 83-87202-98-3.
- [13] Starzak Ł., Napieralski A., Charlot J.-J.: VHDL-AMS: A competitor for SPICE in modeling of semiconductor devices. In: *International Conference “Modern Problems of Radio Engineering, Telecommunications and Computer Science” (TCSET)*. Lviv-Slavske, Ukraine, 18–23 February 2002. Lviv: Lviv Polytechnic National University, 2002, pp. 353–356. ISBN 966-553-234-0.
- [14] Starzak Ł., Zubert M., Napieralski A.: The new approach to the power semiconductor devices modeling. In: *Technical Proceedings of the NanoTech 2002 International Conference on Modeling and Simulation of Microsystems*. San Juan, Puerto Rico, USA, 21–25 April 2002. Boston: Computational Publications, 2002, pp. 640–

644. ISBN 0-9708275-7-1.

- [15] Świercz B., Starzak Ł., Zubert M., Napieralski A.: Zastosowanie internetu do interaktywnych obliczeń pakietami CAD. In: *X Konferencja „Sieci i Systemy Informatyczne” (SiS). Łódź, Poland, October 2002*. Łódź: Piątek Trzynastego, 2002, pp. 217–227. ISBN 83-88742-44-2.
- [16] Starzak Ł., Świercz B., Zubert M., Napieralski A.: SPICE-based simulation website: application to teaching of power electronics. In: *Proceedings of the 7th International Conference “The Experience of Designing and Application of CAD Systems in Microelectronics” (CADSM). Lviv-Slavske, Ukraine, 18–22 February 2003*. Lviv: Lviv Polytechnic National University, 2003, pp. 334–336. ISBN 966-553-278-2.
- [17] Świercz B., Starzak Ł., Zubert M., Napieralski A.: An interactive website as a tool for CAD of power circuits. In: *Technical Proceedings of the 2003 Nanotechnology Conference and Trade Show (NanoTech). Volume 2. San Francisco, CA, USA, 23–27 February 2003*. Boston: Computational Publications, 2003, pp. 346–349. ISBN 0-9728422-1-7.
- [18] Starzak Ł., Świercz B., Zubert M., Napieralski A.: Web-based simulation of power circuits for design and teaching. In: *10th European Conference on Power Electronics and Applications (EPE). Toulouse, France, 2–4 September 2003* [CD-ROM].
- [19] Górecki K., Kamiński M., Starzak Ł.: Elektrotermiczny model układu scalonego UCY75450 dla programu SPICE. In: *Mikroelektronika i informatyka: Prace naukowe Katedry Mikroelektroniki i Technik Informatycznych PŁ. Nr 3/2003*. Łódź: Katedra Mikroelektroniki i Technik Informatycznych Politechniki Łódzkiej, 2003, pp. 63–69. ISBN 83-919289-3-4.
- [20] Jabłoński G., Starzak Ł., Poźniak T., Makowski D., Bek S.: Przekształtnik do laboratoryjno-eksperymentalnego systemu fotowoltaicznego. In: *Mikroelektronika i informatyka: Prace naukowe. Konferencja Katedry Mikroelektroniki i Technik Informatycznych Ślesin 2004. Zeszyt nr 4*. Łódź: Katedra Mikroelektroniki i Technik Informatycznych Politechniki Łódzkiej, 2004, pp. 55–58. ISBN 83-919289-5-0.
- [21] Starzak Ł.: Zredukowany fizyczny model diody mocy dla symulatora SPICE. In: *VI International Workshop for Candidates for a Doctor’s Degree (OWD). Wisła, Poland, 16–19 October 2004. Conference Archives PTETiS, vol. 19*. [Gliwice]: Komitet Organizacyjny Sympozjum PPEE i Seminarium BSE, 2004, pp. 471–475. ISBN 83-915991-8-3.
- [22] Świercz B., Starzak Ł., Zubert M., Napieralski A.: Zdalne nauczanie a zaawansowane metody symulacji układów elektronicznych. In: *Poznańskie Warsztaty Telekomunikacyjne (PWT). Poznań, Poland, 9–10 December 2004*. Poznań: Instytut Elektroniki i Telekomunikacji Politechniki Poznańskiej, 2004, pp. 81–83. ISBN 83-912916-3-4.
- [23] Bek S., Starzak Ł., Makowski D., Jabłoński G.: Measurements of 500 W Power Factor Correction Boost Converter. In: *Proceedings of the VIIIth International Conference CADSM 2005. Lviv-Polyana, Ukraine, 23–26 February 2005*. Lviv: Publishing House of Lviv Polytechnic National University, 2005, pp. 90–92. ISBN 966-553-431-9.
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- [26] Starzak Ł., Napieralski A.: A new IGBT model based on distributed PIN model for SPICE. In: *Proceedings of the International Conference Mixed Design of Integrated Circuits and Systems: MIXDES 2006. Gdynia, Poland, 22–24 June 2006*. [Łódź]: Department of Microelectronics and Computer Science, 2006, pp. 603–606. ISBN 83-922632-1-9.
- [27] Janicki M., Makowski D., Kędziora P., Starzak Ł., Jabłoński G., Bek S.: Improvement of PFC boost converter energy performance using silicon carbide diode. In: *Proceedings of the International Conference Mixed Design of Integrated Circuits and Systems: MIXDES 2006. Gdynia, Poland, 22–24 June 2006*. [Łódź]: Department of Microelectronics and Computer Science, 2006, pp. 615–618. ISBN 83-922632-1-9.
- [28] Kędziora P., Makowski D., Starzak Ł., Janicki M., Bek S.: Student laboratory stand for investigation of SiC diode based boost power converter. In: *8th International Seminar on Power Semiconductors: ISPS ’06: Proceedings. Prague, Czech Republic, 29 August – 1 September 2006*. Praha: České centrum IEE, 2006, pp. 199–203. ISBN 80-

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- [29] Starzak Ł., Simiński S., Bek S.: Stanowisko do badań właściwości dynamicznych polowych przyrządów półprzewodnikowych mocy w typowych konfiguracjach układowych. In: *Mikroelektronika i informatyka: Zeszyty naukowe Katedry Mikroelektroniki i Technik Informatycznych. Zeszyt nr 7*. Łódź: Katedra Mikroelektroniki i Technik Informatycznych Politechniki Łódzkiej, 2007, pp. 113–116. ISBN 83-922632-5-1.
- [30] Starzak Ł., Bek S.: Modelowanie lamp fluorescencyjnych dla celów projektowania stateczników elektronicznych. In: *Mikroelektronika i informatyka: Zeszyty naukowe Katedry Mikroelektroniki i Technik Informatycznych. Zeszyt nr 7*. Łódź: Katedra Mikroelektroniki i Technik Informatycznych Politechniki Łódzkiej, 2007, pp. 117–121. ISBN 83-922632-5-1.
- [31] Napieralski A., Napieralska M., Zubert M., Jabłoński G., Starzak Ł.: Static electro-thermal model of SiC merged PiN Schottky diodes. In: *10th International Seminar on Power Semiconductors: ISPS '10. Prague, Czech Republic, 1–3 September 2010*.
- [32] Zubert M., Starzak Ł., Jablonski G., Napieralska M., Janicki M., Napieralski A.: The accurate electro-thermal model of merged SiC PiN Schottky diodes. In: *Nanotechnology 2011: Electronics, Devices, Fabrication, MEMS, Fluidics and Computational*. Vol. 2. Chapter 10: Compact Modeling, pp. 796–799. ISBN 978-1-4398-7139-3
- [33] Zubert M., Starzak Ł., Jabłoński G., Napieralska M., Janicki M., Napieralski A.: Novel SPICE dynamic model of SiC Merged PiN Schottky diodes. In: *Proceedings of the 18th International Conference Mixed Design of Integrated Circuits and Systems. Gliwice, 16-18 June 2011*. Łódź: Politechnika Łódzka, 2011, pp. 541–544. ISBN 978-83-932075-0-3.
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- [35] Moreno Galan D., Starzak Ł., Torzewicz T., Piotrowicz M., Marańda W.: Laboratory setup for investigation of MPPT algorithms of photovoltaic modules under non-uniform insolation. In: *Proceedings of the 20th International Conference Mixed Design of Integrated Circuits and Systems MIXDES 2013. Gdynia, Poland, 20-22 June 2013*. Łódź: Lodz University of Technology, 2013, pp. 603–606. ISBN 978-83-63578-01-5.
- [36] Janicki M., Torzewicz T., Kulesza Z., Starzak L., Napieralski A.: Influence of boundary conditions on thermal RC ladder model element values. In: *29th Annual IEEE Semiconductor Thermal Measurement and Management Symposium, SEMI-THERM, Proceedings 2013. San Jose, CA, USA, March 17–21, 2013*. Institute of Electrical and Electronics Engineers, 2013, pp. 71–75. ISBN 978-1-4673-6429-4.

Book Chapters

- [37] Napieralski A., Napieralska M., Starzak Ł.: Power Devices. In: *Power/HV MOS Devices Compact Modeling* (red. Władysław Grabinski, Thomas Gneiting). Springer Science+Business Media, 2010, Chapter 5, pp. 129–148. ISBN 978-90-481-3045-0.
- [38] Napieralski A., Starzak Ł., Świercz B., Zubert M.: Web-Based Modelling Tools. In: *Power/HV MOS Devices Compact Modeling* (red. Władysław Grabinski, Thomas Gneiting). Springer Science+Business Media, 2010, Chapter 7, pp. 183–198. ISBN 978-90-481-3045-0.

Course Books

- [39] Napieralska M., Jabłoński G., Starzak Ł.: *Laboratorium podstaw mikroelektroniki*. Łódź: Politechnika Łódzka, 2007. 51 s. ISBN 83-922632-3-5.

Reports

- [40] Bek S., Kamiński M., Starzak Ł., Szajfler A.: *Sprawozdanie z wykonania pracy badawczo-rozwojowej w ramach umowy z Katedrą Radioelektroniki Morskiej Akademii Morskiej w Gdyni z dn. 6.01.2003 – Pomiary wpływu wzajemnych oddziaływań elektrotermicznych w układzie UCY75450*. Raport wewnętrzny. Łódź: Politechnika Łódzka. Katedra Mikroelektroniki i Technik Informatycznych, 18 grudnia 2003.
- [41] Starzak Ł., Jabłoński G.: *Pomiary diod SiC. Raport z realizacji projektu badawczego rozwojowego 0312/R/T02/2008/04*. Raport wewnętrzny. Łódź: Politechnika Łódzka. Katedra Mikroelektroniki i Technik Informatycznych, 6 marca 2010.

- [42] Napieralski A., Zubert M., Napieralska M., Starzak Ł.: *Raport z wykonania prac w ramach umowy z Katedrą Elektroniki Morskiej Akademii Morskiej w Gdyni z dn. 8.11.2012: Pomiary, analiza i modelowanie nowoczesnych przyrządów półprzewodnikowych mocy z węgla krzemu ze szczególnym uwzględnieniem zjawisk elektrotermicznych*. Raport wewnętrzny. Łódź: Politechnika Łódzka. Katedra Mikroelektroniki i Technik Informatycznych, 28 grudnia 2012.

Other

- [43] Starzak Ł., Wiosetek-Reske A.: Prawo bez prawa. *Forum Akademickie*. Nr 1/2004, pp. 30–31. ISSN 1233-0930.
- [44] Starzak Ł.: Elektronicy z PŁ pomocą we wdrażaniu nowoczesnych źródeł światła. *Życie Uczelni. Biuletyn Informacyjny Politechniki Łódzkiej*. Nr 104 (2), czerwiec 2008, s. 23. ISSN 1425-4344.

RESEARCH PROJECTS

Completed Projects

1. *Environment for design and investigation of switched-mode power electronic circuits*. Internal Department grant. Researcher. (03/2004–12/2004)
2. *Investigation of interfacing of switched-mode power electronic circuits with real supply grid*. Internal Department grant. Researcher. (03/2005–12/2005)
3. *Investigation of dynamic processes in power semiconductor devices: measurement methods and their validation*. Internal Department grant. Researcher. (03/2006–12/2006)
4. *Fluorescent lamp models and their verification*. Internal Department grant. Researcher. (03/2007–12/2007)
5. *Support Mechanism for Innovative Activity of Doctoral Students [Mechanizm Wspierania Innowacyjnej Działalności Doktorantów] (WIDDOK)*. Project funded by the European Social Fund and Polish State within the Integrated Regional Development Programme; action line: Regional Innovation Strategies and Transfer of Knowledge; no. Z/2.10/II/2.6/04/05. Proposal co-author; employee. (2006–2008)
6. *Power electronic circuits with silicon carbide devices [Układy energoelektroniczne z przyrządami z węgliku krzemu]*. Polish Ministry of Science and Higher Education grant no. 0312/R/T02/2008/04. Proposal co-author; researcher. (1/7/2008–30/6/2011)

Projects in Realisation

7. *Developing Multidomain MEMS Models for Educational Purposes (EduMEMS)*. International project funded by the European Union within the 7th Framework Programme, People Programme, Marie Curie Actions, International Research Staff Exchange Scheme (MC-IRSES); no. 269295. Researcher. (1/7/2011–30/6/2015)
8. *Advanced Electric Powertrain Technology (ADEPT)*. International project funded by the European Union within the 7th Framework Programme, People Programme, Marie Curie Actions, Networks for Initial Training (MC-ITN); no. 607361. Proposal co-author, researcher, Supervisory Board member. (1/10/2013–30/9/2017)

Submitted Proposals

9. *Modelling of high power semiconductor devices with special consideration of spatial parameters of their structures [Modelowanie półprzewodnikowych przyrządów dużej mocy ze szczególnym uwzględnieniem parametrów przestrzennych struktury]*. Submitted to the Polish State Committee for Scientific Research. Proposal author. (2005)
10. *Application of photovoltaics to interior daytime lighting with sunlight deficit compensation [Zastosowanie fotowoltaiki do dziennego oświetlenia pomieszczeń z kompensacją deficytów światła naturalnego]*. Submitted to the Polish Ministry of Science and Higher Education. Proposal co-author. (2010)
11. *Study on application of photovoltaics for interior daytime lighting under highly variable solar radiation [Badanie zastosowań fotowoltaiki do dziennego oświetlenia pomieszczeń w warunkach silnej zmienności nasłonecznienia]*. Submitted to the Polish National Science Centre. Proposal co-author. (2011)
12. *Open-source based training and research platform for electrical drives in green vehicle systems (GREENDRIVE)*. Submitted to the European Commission for funding within the 7th Framework Programme, People Programme, Marie Curie Actions, Networks for Initial Training (MC-ITN). Proposal co-author. (2012)

TEACHING ACTIVITIES

Present Teaching

1. *Power Devices and Circuits [Przyrządy i układy mocy]*, laboratory laboratory coordinator (80–120 students/year), co-author of laboratory exercises and experimental set-ups 2013/14, 2012/13, 2011/12, 2010/11, 2009/10, 2008/09
2. *Power Devices and Systems* (taught in English), laboratory co-author of laboratory exercises and experimental set-ups 2013/14, 2012/13, 2011/12, 2010/11, 2009/10, 2008/09
3. *Power Components and Integrated Circuits [Podzespoły i układy scalone mocy]*, lecture and laboratory lecture co-author, author of laboratory exercises and experimental set-ups 2013/14, 2012/13, 2011/12, 2010/11, 2009/10
4. *Power Electronic Converters [Przekształtniki elektroniczne]*, laboratory co-author of laboratory exercises and experimental set-ups 2013/14, 2011/12, 2010/11, 2009/10
5. *Control of Power Electronic Converters [Sterowanie przekształtników elektronicznych]* (two-cycle curriculum), lecture and laboratory course coordinator, lecture author, author of laboratory exercises and experimental set-ups 2011/12, 2010/11
6. *Electrical Power Conversion in Photovoltaics [Przetwarzanie energii elektrycznej w fotowoltaice]*, lecture course coordinator, lecture author, co-author of laboratory exercises and experimental set-ups 2013/14, 2012/13, 2011/12
7. *Parameter Optimisation of Power Converters [Optymalizacja parametrów przekształtników]*, lecture lecture co-author, co-author of laboratory exercises and experimental set-ups 2012/13, 2011/12
8. *Digital Control of Switched-Mode Power Converters [Cyfrowe sterowanie przekształtników impulsowych]*, lecture course coordinator, lecture author, author of laboratory exercises 2012/13, 2011/12
9. *Measurements and Modelling in Power Electronics [Pomiary i modelowanie w elektronice mocy]*, lecture course coordinator, lecture co-author, co-author of laboratory exercises and experimental set-ups
10. *Programming Fundamentals [Bases de la programmation]* (taught in French), lecture and laboratory course coordinator, lecture author, co-author of laboratory exercises 2013/14, 2012/13
11. *Programming Fundamentals 2 [Bases de la programmation 2]* (taught in French), lecture and laboratory course coordinator, lecture author, author of laboratory exercises 2012/13, 2011/12, 2010/11, 2009/10, 2008/09

Other Course Coordination

12. *Component Blocks of Photovoltaic Systems [Podzespoły systemów fotowoltaicznych]* – course coordinator, co-author of project topics
13. *Switched-Mode Power Circuits and Components [Impulsowe układy i elementy mocy]* – course coordinator, programme author
14. *Analogue and Mixed Controllers for Power Converters [Analogowe i mieszane sterowniki przekształtników]* – course coordinator, programme author

Past Teaching

15. *Parameter Optimisation in Power Electronic Systems [Optymalizacja parametrów układów elektroniki przemysłowej]*, lecture lecture co-author, co-author of laboratory exercises and experimental set-ups 2010/11

16. *Control of Power Electronic Converters [Sterowanie przekształtników elektronicznych]* (integrated master curriculum), lecture and laboratory
lecture author, author of laboratory exercises
2010/11, 2009/10, 2008/09, 2007/08
17. *Power Semiconductor Devices [Przyrządy półprzewodnikowe mocy]* (Electronics and Telecommunications field of study), laboratory
co-author of laboratory exercises and experimental set-ups
2008/09, 2007/08, 2006/07, 2005/06, 2004/05, 2003/04, 2002/03
18. *Fundamentals of Power Electronics [Podstawy energoelektroniki]*, laboratory
co-author of laboratory exercises and experimental set-ups
2005/06, 2004/05, 2003/04, 2002/03
19. *Power Electronics [Energoelektronika]*, laboratory
2003/04
20. *Computer-Aided Design of Electronic Circuits [Komputerowe projektowanie układów elektronicznych]*, lecture and laboratory
lecture author, author of laboratory exercises
2004/05, 2003/04, 2002/03
21. *Microelectronics* (taught in English), laboratory
co-author of teaching materials
2008/09, 2007/08, 2006/07, 2005/06
22. *Introduction to Microelectronics [Wprowadzenie do mikroelektroniki]*, laboratory
co-author of teaching materials
2006/07, 2004/05, 2003/04, 2002/03
23. *Fundamentals of Microelectronics [Podstawy mikroelektroniki]*, laboratory
co-author of teaching materials
2005/06, 2004/05, 2002/03

Other Courses Co-Authored

24. *Power Semiconductor Devices [Przyrządy półprzewodnikowe mocy]* (Mechatronics field of study) – co-author of laboratory exercises and experimental set-ups
25. *Elektroniczne układy sterowania nastawników [Elektroniczne układy sterowania nastawników]* – co-author of laboratory exercises and experimental set-ups

BSc and MSc Theses Supervised (Completed)

- [1] *An interactive application in Java demonstrating the operation of high power semiconductor devices using multimedia presentations*: K. Fiks; MSc thesis (2006)
- [2] *Electronic power supply circuits for fluorescent lamps*: M. Sońta; BSc thesis (2006)
- [3] *Design and realization of a DC/AC converter for education*: S. Simiński; BSc thesis (2007)
- [4] *Pulse measurements of forward voltage-current characteristics of power semiconductor devices*: P. Stępniaik; BSc thesis (2007)
- [5] *Design and realization of a 1-phase/3-phase rectifier for demonstration purposes*: M. Jędrzejewski; BSc thesis (2007)
- [6] *Fluorescent lamp electronic ballasts with power factor correction*: P. Zieliński; MSc thesis (2007)
- [7] *Fluorescent lamp model and its verification*: A. Sobierajski; BSc thesis (2007)
- [8] *Decision algorithms in digital protection automation systems based on the example of compressor supply*: A. Zeja; BSc thesis (2007)
- [9] *Integral cycle controller with temperature regulation for a resistive heater*: P. Wojda; BSc thesis (2007)
- [10] *A 2 kHz, 10 A switched mode amplifier*: M. Jagiełło; BSc thesis (2007)
- [11] *Design and realization of a DC motor drive circuit for demonstration purposes*: Ł. Rudkowski; MSc thesis (2007)

- [12] *Design and realization of an AC/AC converter for demonstration purposes*: D. Jagodziński; BSc thesis (2008)
- [13] *Design rules of interference filters for switched-mode power supplies with power factor correction*: R. Bobik; MSc thesis (2008)
- [14] *Modification of a loose material mill control system*: T. Wojciechowski; BSc thesis (2008)
- [15] *Design and prototyping of an uninterruptible computer power supply*: K. Busiakiewicz; MSc thesis (2009)
- [16] *Very small dimensions 230 V AC to low DC voltage converter*: M. Cyganek; MSc thesis (2009)
- [17] *Design and prototyping of a wind power generator for camping*: P. Celejewski; MSc thesis (2009)
- [18] *Electronic circuit schematic editor for OpenOffice*: A. Kubacki; MSc thesis (2009)
- [19] *Input EMI filters and their influence on the stability of electronic converters*: A. Krzywoszonek; MSc thesis (2009)
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1. Outstanding Paper Award, 8th International Conference Mixed Design of Integrated Circuits and Systems; Ł. Starzak, M. Zubert, A. Napieralski, P. Austin, G. Bonnet, Th. Bordinon, M. Marmouget, J.-L. Sanchez (2001)
2. IEE Best Paper Award, VI International Workshop for Candidates for Doctor's Degree (2004)
3. Outstanding Paper Award, 13th International Conference Mixed Design of Integrated Circuits and Systems; Ł. Starzak, A. Napieralski (2006)
4. Diploma for awarded thesis supervisor, Best MSc Thesis Contest, Association of Polish Electrical Engineers (2010)
5. Diploma for awarded thesis supervisor, Best BSc Thesis Contest, Association of Polish Electrical Engineers (2011)
6. 2nd Degree Rector's Team Award for "Design of electronic systems containing MEMS structures with special consideration of thermal phenomena and multi-core processor architectures"; M. Janicki, M. Zubert, Ł. Starzak, M. Szermer, P. Zajac (2011)
7. 3rd Degree Rector's Team Award for "Methods of energetic efficiency improvement in selected aspects of electrical energy generation and conversion"; W. Marańda, P. Pietrzak, Ł. Starzak, S. Wróblewski (2012)