

PSpice circuit description guidelines

by Łukasz Starzak, version 2008

File structure

Title line (not interpreted)

* Comment lines begin with an asterisk

* Device definitions

* ...

* Model library

.LIB *library_filename*

* DC analysis command with linear step

.DC LIN *source_name start end step*

* DC nested analysis

.DC LIN *x_axis_source start end step curve_family_source start end step*

* Temperature change

.TEMP *T1 T2 T3 ...*

* Parameter declaration

.PARAM *name=value*

* Parameter usage in device declarations: {*name*}

* Parameter logarithmic change

.STEP OCT PARAM *name start end points_per_double_change*

* Parameter change from list

.STEP PARAM *name LIST value1 value2 value3 ...*

* Transient analysis command

.TRAN *print_step_(irrelevant) end_time plot_start_time max_time_step*

* Generate data for Probe

.PROBE

* Circuit description end, put one (and not more) ENTER after this line

.END

Device declarations

MOSFET transistor:

Mname drain gate source bulk model_name L=channel_length W=channel_width

DC voltage source:

Vname pos neg value

Pulse voltage source:

Vname pos neg PULSE(idle_value pulse_value delay rise fall pulse_width period)